AMENDMENTS TO THE CLAIMS

Listing of claims:

Following is a listing of all claims in the present application, which listing supersedes all previously presented claims:

1. (Original) A diversity receiving apparatus that separately weights reception signals of a plurality of reception systems using combining coefficients based on a respective amplitude component of each reception signal, combines the weighted reception signals, extracts symbol sections in the combined reception signals, and generates a clock for detecting symbols,

the diversity receiving apparatus comprising:

judging means for judging whether every combining coefficient is below a predetermined threshold;

multiplying means for uniformly multiplying every combining coefficient when the judging means judges that every combining coefficient is below the predetermined threshold; and

combining means for combining the reception signals using the multiplied combining coefficients.

2. (Original) The diversity receiving apparatus of claim 1,

wherein the combining coefficients are one of an received signal strength for each reception system and a parameter showing a reliability of the reception signal obtained by each reception system

3. (Original) The diversity receiving apparatus of claim 2,

wherein the multiplying means multiplies every combining coefficient by a constant when the judging means judges that every combining coefficient is below the predetermined threshold.

4. (Currently Amended) The diversity receiving apparatus of claim 3,

wherein the constant is related to a result of dividing a maximum value for the combining coefficients by the predetermined threshold.

5. (Currently Amended) The diversity receiving apparatus of claim 4,

wherein each combining coefficient is expressed using a predetermined number of bits,

the constant being 2^n and the predetermined threshold being found by dividing the ~~a~~ maximum value that can be expressed using the predetermined number of bits by 2^n, where 1 is $\leq n <$ the predetermined number of bits and n is an integer.

6. (Original) A diversity receiving apparatus that separately weights reception signals of a plurality of reception systems using combining coefficients based on a receptive amplitude component of each reception signal, combines the weighted reception signals, extracts symbol sections in the combined reception signals, and generates a clock for detecting symbols,

the diversity receiving apparatus comprising:

judging means for judging whether every combining coefficient is below a predetermined threshold;

multiplying means for uniformly multiplying every combining coefficient when the judging means judges that every combining coefficient is below the predetermined threshold;

combining means for combining the reception signals using the multiplied combining coefficients; and

generating means for generating a clock that is synchronized with the reception signals of the reception system using the reception signals combined by the combining means.

7. (Original) The diversity receiving apparatus of claim 6,

wherein the combining coefficients are one of an received signal strength for each reception system and a parameter showing a reliability of the reception signal obtained by each reception system.

8. (Original) The diversity receiving apparatus of claim 7,

wherein the multiplying means multiplies every combining coefficient by a constant when the judging means judges that every combining coefficient is below the predetermined threshold.

9. (Currently Amended) The diversity receiving apparatus of claim 8,

wherein the constant is related to a result of dividing a maximum value for the combining coefficients by the predetermined threshold.

10. (Currently Amended) The diversity receiving apparatus of claim 9,

wherein each combining coefficient is expressed using a predetermined

number of bits,

the constant being 2^n and the predetermined threshold being found by dividing the ~~a~~ maximum value that can be expressed using the predetermined number of bits by 2^n, wherein $1 \leq n <$ the predetermined number of bits and n is an integer.

11. (Original) A diversity receiving apparatus that separately weights reception signals of a plurality of reception systems using combining coefficients based on a respective amplitude component of each reception signal, combines the weighted reception signals, and generates a clock for detecting symbols based on the combined reception signals,

the diversity receiving apparatus comprising:

judging means for judging whether every combining coefficient is below a predetermined threshold;

multiplying means for doubling every combining coefficient when the judging means judges that every combining coefficient is below the predetermined threshold;

control means for repeating activating the judging means and multiplying means until the judging means judges that at least one of the combining coefficients is no longer below the predetermined threshold;

combining means for combining the reception signals using the multiplied combining coefficients when the judging means judges at least one of the combining coefficients is no longer below the predetermined threshold; and

generating means for generating a clock that is synchronized with the reception signals of the plurality of reception systems using the reception signals combined by the combining means.

12. (Original) The diversity receiving apparatus of claim 11,

wherein the combining coefficients are one of an received signal strength for each reception system and a parameter showing a reliability of the reception signal obtained by each reception system.

13. (Original) A clock generating circuit for use by a diversity receiving apparatus that separately weights reception signals of a plurality of reception systems using combining coefficients based on a respective amplitude component of each reception signal and combines the weight reception signals,

the clock generating circuit comprising:

judging means for judging whether every combining coefficient is below a predetermined threshold;

multiplying means for multiplying every combining coefficient when the judging means judges that every combining coefficient is below the predetermined threshold;

combining means for combining the reception signals using the multiplied combining coefficients; and generating means for generating a clock that is synchronized with the reception signals of the plurality of reception systems using the reception of signals combined by the combining means.